
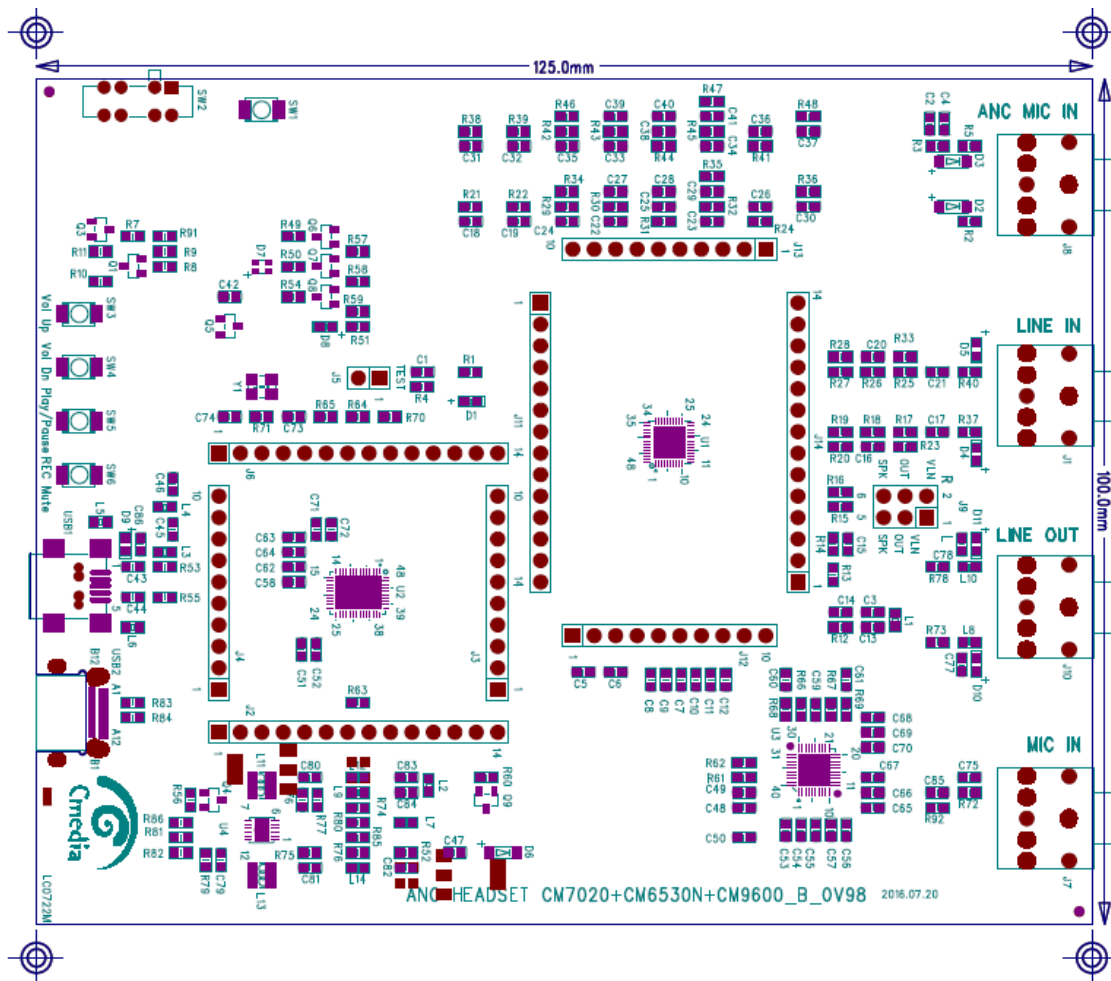



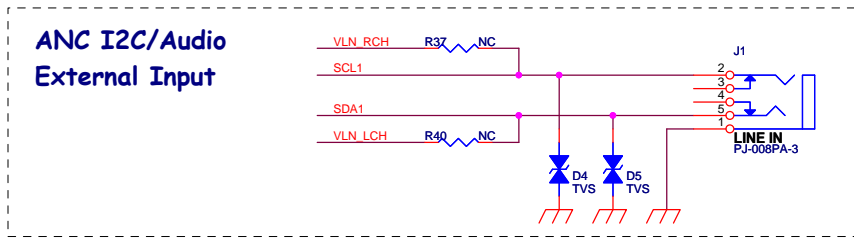
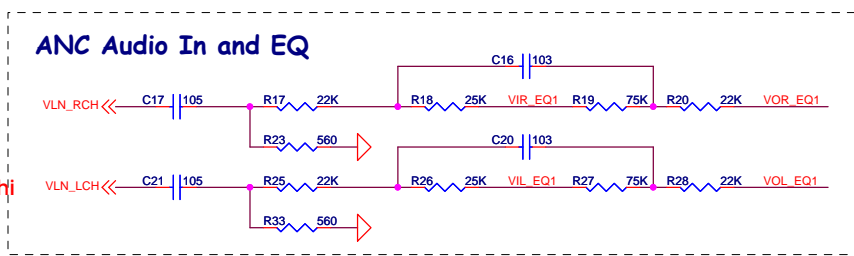
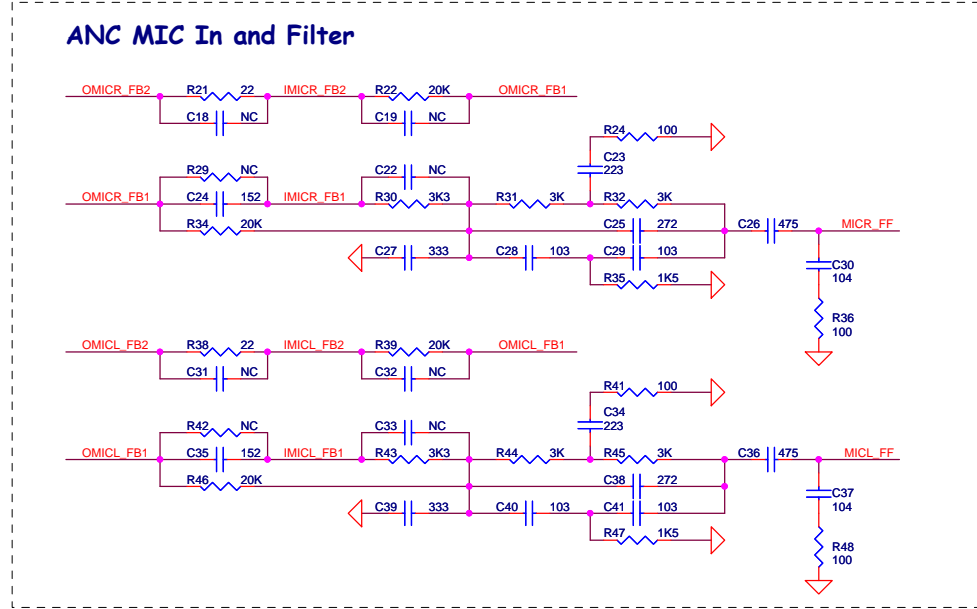
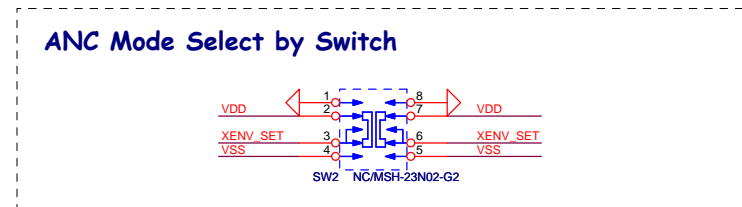
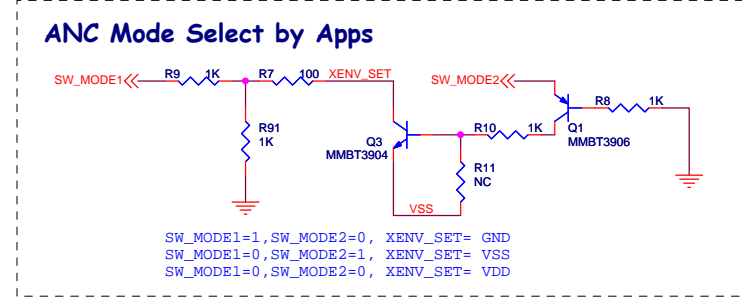
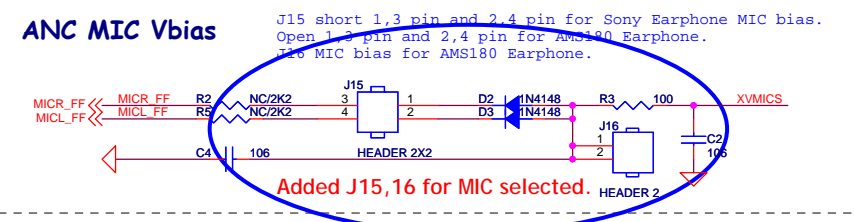
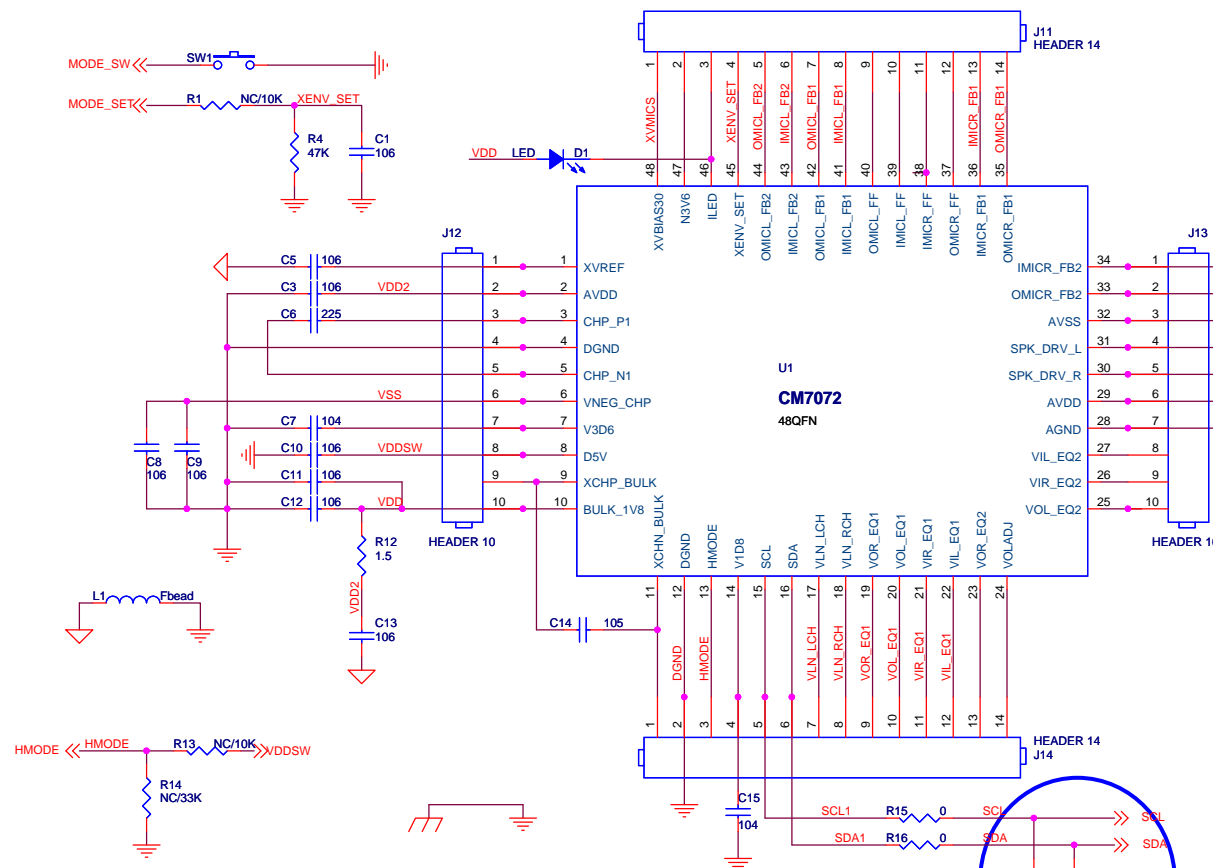
2015/07/22	0V90	1. Initial Version	Lion, Tsai
2015/07/24	0V91	1. Replaced CM6530N to CM6531N	Lion, Tsai
2015/08/26	0V92	1. Replaced CM6531N Package	Lion, Tsai
2015/09/04	0V93	1. Replaced CM6531N Package	Lion, Tsai
2015/09/14	0V94	1. Removed ANC Mode Switch 2. Replaced DC to DC 3.3V to CM6531N 3. Replaced DC to DC 1.8V to CM7072 4. Removed ANC EQ Circuit	Lion, Tsai
2015/10/21	0V95	1. Re-Design for Requirement	Lion, Tsai
2015/11/17	0V96	1. Re-Design for Requirement 2nd	Lion, Tsai
2016/04/08	0V97	1. Re-Design for Requirement 3rd 2. Fixed GPIO Issue 3. Improved MIC in Circuit 4. Added U3 Connector	Lion, Tsai
2016/05/10	0V971	1. Added MIC-IN Vbias Filter. 2. Cost Down ANC Mode Select Transistor 3. Adjusted Capacitance to ANC	Lion, Tsai
2016/07/13	0V98	1. C24, C35 modify to 152. 2. C26, C38 modify to 272. 3. C27, C39 modify to 333. 4. R34, R46 modify to 20KOhm. 5. R51, R52, R53 modify to 5K1 Ohm. 6. Added C86=22uF	Brad, Wu
2016/08/02	0V99	1. Added R93,R94 for IIC pull Hi 2. R13,R14 NC	Lion, Tsai
2017/01/23	1V00	1. Updated placement. 2. Remove R2, R5 for AMS180 earphone. 3. Modify R84 5K1 for type C device.	Brad, Wu
2017/03/08	1V01	1. Added R93, R94. 2. Added Socket for CM6530N. 3. Added J15,16 for MIC selected.	Lion, Tsai



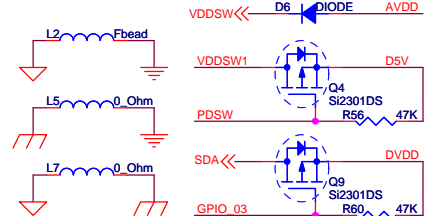
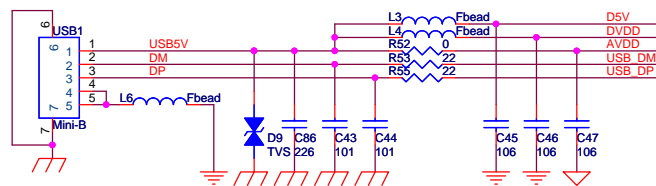
 C-MEDIA ELECTRONICS Inc. 6F., 100, Sec. 4, Civil Boulevard, Taipei, Taiwan, R.O.C.		
Title		
Release Note		
Size	Document Number	Rev
	CM7072+CM6530N+CM9600_Schematic B	1.01
Date:	Tuesday, March 07, 2017	
	Sheet	1 of 5



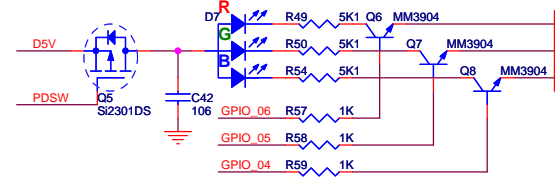
 C-MEDIA ELECTRONICS Inc. 6F., 100, Sec. 4, Civil Boulevard, Taipei, Taiwan, R.O.C.	
Title	
Placement	
Size	Document Number
	CM7072+CM6530N+CM9600_Schematic B
Date: Tuesday, March 07, 2017	Sheet 2 of 5
	Rev 1.01



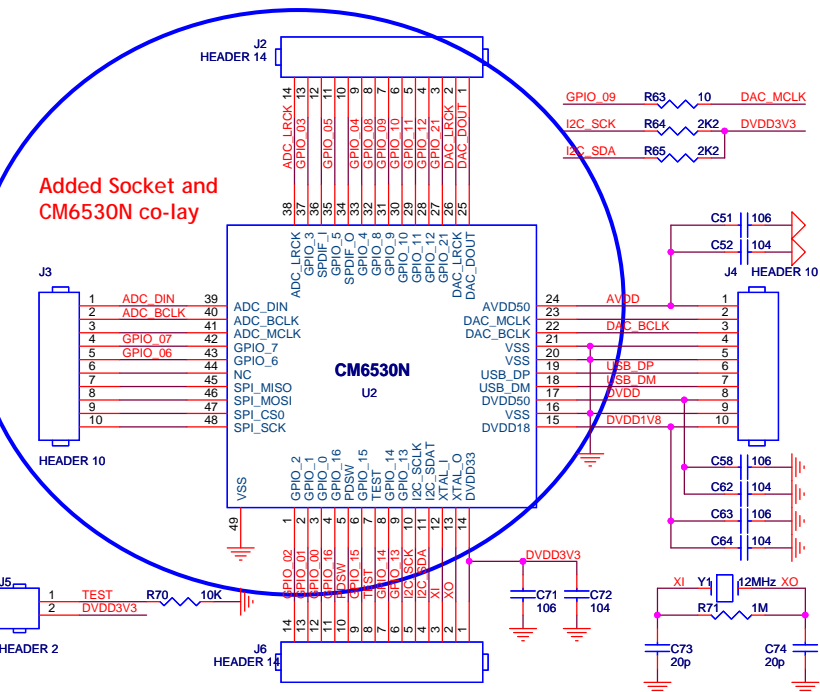
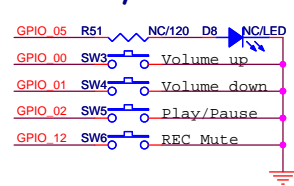
USB Connector



3 Color LED



HID Key

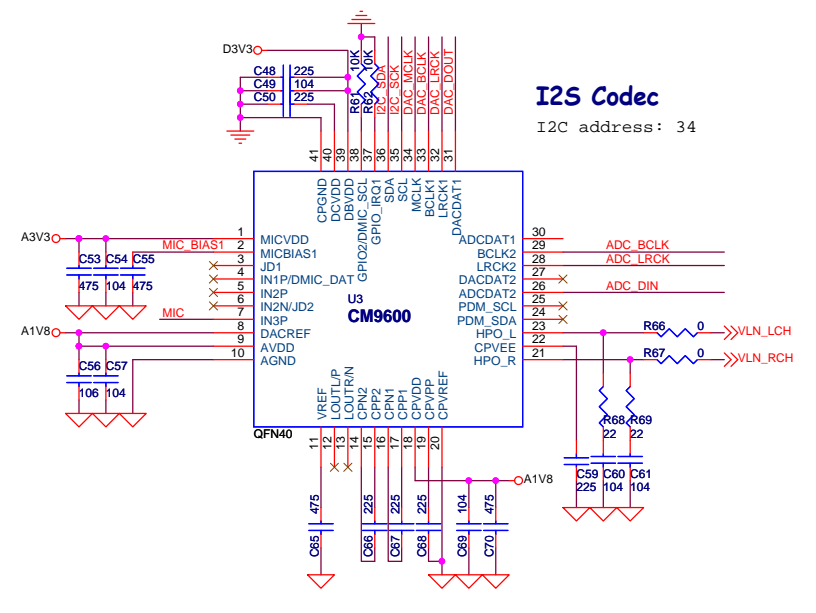


HMODE<< GPIO 07
MODE_SE<< GPIO 08
SW_MODE1<< GPIO 10
SW_MODE2<< GPIO 11
SCI<< GPIO 14
MODE_SW<< GPIO 15
VDDSW1<< GPIO 16

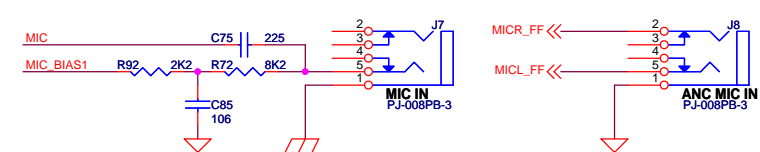
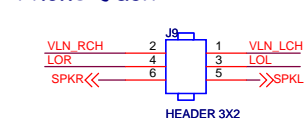
GPIO00 Volume up Button
GPIO01 Volume down Button
GPIO02 Play/Pause Button
GPIO03 ANC Control OTP 5V
GPIO04 LED_B
GPIO05 LED_G
GPIO06 LED_R
GPIO07 ANC Programming
GPIO08 ANC Cycle Mode Control
GPIO09 24M Hz Clock to 9600
GPIO10 ANC Switch Mode bit 0
GPIO11 ANC Switch Mode bit 1
GPIO12 EQ mode Button
GPIO13 Spare/RT8020 Control
GPIO14 I2C SDA
GPIO15 I2C SCL
GPIO16 ANC cycle mode button
GPIO21 Spare
Test Pin 0 ohm to Vcc (default NC)

I2S Codec

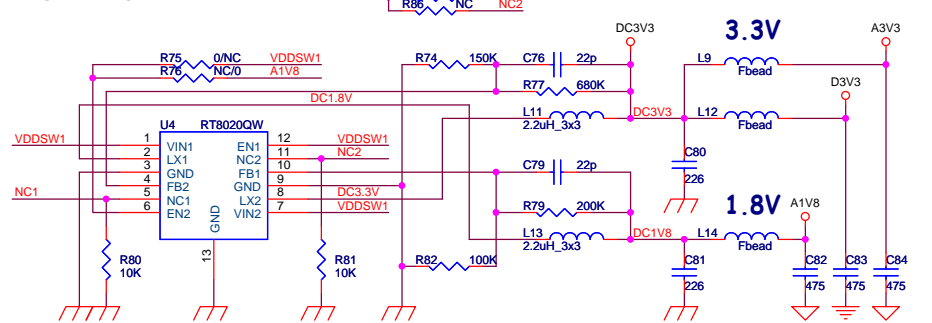
I2C address: 34



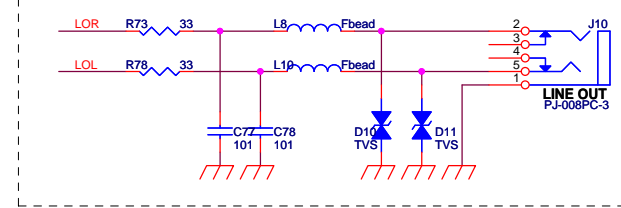
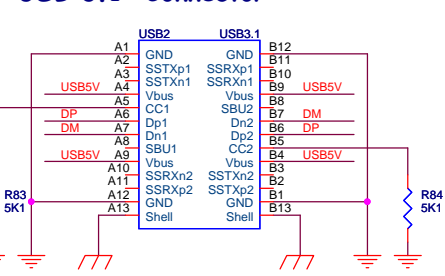
Phone Jack



DC to DC Power



USB 3.1 Connector



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Title
CM6530N Main
Size Document Number
CM7072+CM6530N+CM9600_Schematic B
Date: Wednesday, March 08, 2017 Sheet 4 of 5

